

103-4198<sup>co</sup>  
162-234<sup>00</sup>

Docket: 0756-1551

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of )  
 Shunpei YAMAZAKI et al. )  
 Serial No. 08/691,434 ) Art Unit: 2822  
 Filed: August 2, 1996 ) Examiner: M. Wilczewski  
 For: METHOD OF FABRICATING )  
 SEMICONDUCTOR DEVICES AND )  
 APPARATUS FOR PROCESSING A )  
 SEMICONDUCTOR )

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on \_\_\_\_\_.

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TECHNOLOGY CENTER - 2800

Honorable Assistant Commissioner of Patents  
 Washington, D.C. 20231

03/02/1999 JMC/MILLA 00000011 08691434

Sir:

01 FC:103  
 02 FC:102

198.00 CP

234.00 00

Transmitted herewith is a Supplemental Amendment in the above-identified application.

	Claims Remaining After Amendment		Highest Number Previously Paid For	Present Extra	Small/Large Entity Rate	Fee
Total	85	Minus	74	11	x \$9/\$18	\$198.00
Indep.	15	Minus	12	3	x \$39/\$78	\$234.00
First Presentation of Multiple Dependent Claim					+ \$130/\$260	\$0.00
TOTAL						\$432.00

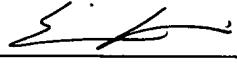
[X] A check in the amount of \$432 to cover the additional claims is enclosed.

JT

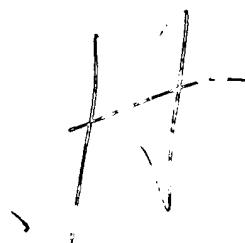
[ X ] In the event applicant(s) has overlooked the need for any petition and fee for extension of time, and such extension is required, applicant(s) requests that this be considered a petition therefor and that such fee be charged to Deposit Account No. 19-2380.

[ X ] The Commissioner is hereby authorized to charge fees under 37 CFR 1.16, 1.17, 1.20(a), 1.20(b), 1.20(c) and 1.20 (d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment, to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,

  
Eric J. Robinson  
Reg. No. 38,285

Sixbey, Friedman, Leedom & Ferguson, P.C.  
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McLean, Virginia 22102  
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SUPPLEMENTAL AMENDMENT

Honorable Assistant Commissioner of Patents  
Washington, D.C. 20231

Sir:

Please amend the above-identified application as follows:

In the claims:

Please amend claims 16, 56 and 74 as follows:

*Amend 54/60 J1>*  
16. (Amended) An apparatus for processing a semiconductor provided on a substrate comprising: